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Mallish

Dkt. 2271/60617

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Yoshinori UEDA

Serial No.: 09/431,593

Group Art Unit: 2815

Date Filed: November 1, 1999

Examiner: Matthew E. Warren

For: SEMICONDUCTOR DEVICE HAVING AN INTEGRAL RESISTANCE ELEMENT

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Paul Teng October 6, 2003
Paul Teng Date
Reg. No. 40,837

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

AMENDMENT

This Amendment is submitted in response to the Office Action dated July 15, 2003 in connection with the above-identified application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the claims are reflected in the Listing of Claims section which begins on page 3 of this paper.

Amendments to the Drawings begin on page 6 of this paper.

Remarks begin on page 7 of this paper.

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